

L Number	Hits	Search text	IPB	Time stamp
1	10	gate adj line\$1 adj driv\$6 and receiv\$6 and predetermin\$5 adj voltage\$1 and vari\$9 and Control\$6 adj signal\$1 and supply\$6	USPAT; EPO; JPO	2004/06/03 14:47
2	0	gate adj line\$1 adj driv\$6 and voltage\$1 and vari\$9 and Control\$6 adj signal\$1 and thermistor	USPAT; EPO; JPO	2004/06/03 14:48
3	0	gate adj line\$1 adj driv\$6 and voltage\$1 and vari\$9 and thermistor	USPAT; EPO; JPO	2004/06/03 14:48
4	0	gate adj line\$1 adj control\$9 and voltage\$1 and vari\$9 and thermistor	USPAT; EPO; JPO	2004/06/03 14:49
5	0	gate adj line\$1 adj control\$9 and thermistor	USPAT; EPO; JPO	2004/06/03 14:49
6	15	gate adj line\$1 and control\$9 and thermistor	USPAT; EPO; JPO	2004/06/03 14:49
7	13	gate adj line\$1 and control\$9 and thermistor and resistor	USPAT; EPO; JPO	2004/06/03 14:49
8	5	gate adj line\$1 and control\$9 and thermistor and resistor and ambient adj temperature	USPAT; EPO; JPO	2004/06/03 14:50
9	4	gate adj line\$1 and control\$9 and thermistor and resistor and ambient adj temperature and voltage and vari\$6	USPAT; EPO; JPO	2004/06/03 15:38
10	0	gate adj line\$1 and control\$9 and thermistor and resistor and ambient adj temperature and voltage and vari\$6 and scan adj line	USPAT; EPO; JPO	2004/06/03 15:39
11	7	scan adj line\$1 and control\$9 and thermistor and resistor and ambient adj temperature and voltage and vari\$6	USPAT; EPO; JPO	2004/06/03 15:39
12	0	scan adj line\$1 and control\$9 and thermistor and resistor and ambient adj temperature and voltage and vari\$6 and gate	USPAT; EPO; JPO	2004/06/03 15:39
13	7	scan adj line\$1 and control\$9 and thermistor and resistor and ambient adj temperature and voltage and vari\$6	USPAT; EPO; JPO	2004/06/03 15:39
14	80	liquid adj crystal adj display and thermistor	USPAT; EPO; JPO	2004/06/03 16:03
15	988	liquid adj crystal adj display and thermistor	USPAT; EPO; JPO	2004/06/03 16:03
16	447	liquid adj crystal adj display and thermistor and resistor	USPAT; EPO; JPO	2004/06/03 16:03
17	436	liquid adj crystal adj display and thermistor and resistor and control\$9	USPAT; EPO; JPO	2004/06/03 16:03
18	414	liquid adj crystal adj display and thermistor and resistor and control\$9 and voltage\$1	USPAT; EPO; JPO	2004/06/03 16:03
19	385	liquid adj crystal adj display and thermistor and resistor and control\$9 and voltage\$1 and change\$9	USPAT; EPO; JPO	2004/06/03 16:04
20	384	liquid adj crystal adj display and thermistor and resistor and control\$9 and voltage\$1 and change\$9 and temperature	USPAT; EPO; JPO	2004/06/03 16:04
21	232	liquid adj crystal adj display and thermistor and resistor and control\$9 and voltage\$1 and change\$9 and temperature and ambient	USPAT; EPO; JPO	2004/06/03 16:04
22	18	liquid adj crystal adj display and thermistor and resistor and control\$9 and voltage\$1 and change\$9 and temperature adj ambient	USPAT; EPO; JPO	2004/06/03 16:05
23	5	liquid adj crystal adj display and thermistor and resistor and control\$9 and voltage\$1 adj change\$9 and temperature adj ambient	USPAT; EPO; JPO	2004/06/03 16:05

24	0	liquid adj crystal adj display and thermistor and resistor and control\$9 and voltage\$1 and change\$9 and temperature adj ambient and scan adj line	USPAT; EPO; JPO	2004/06/03 16:06
25	0	liquid adj crystal adj display and thermistor and resistor and control\$9 and voltage\$1 and change\$9 and temperature adj ambient and gate adj line	USPAT; EPO; JPO	2004/06/03 16:06
26	0	(liquid adj crystal adj display or LCD) and thermistor and resistor and control\$9 and voltage\$1 and change\$9 and temperature adj ambient and gate adj line	USPAT; EPO; JPO	2004/06/03 16:06
27	0	(liquid adj crystal adj display or LCD) and thermistor and resistor and control\$9 and voltage\$1 and change\$9 and temperature adj ambient and gate adj line	USPAT; EPO; JPO	2004/06/03 16:07
28	2120	(liquid adj crystal adj display or LCD) and scan adj line\$1	USPAT; EPO; JPO	2004/06/03 16:07
29	31	(liquid adj crystal adj display or LCD) and scan adj line\$1 and thermistor	USPAT; EPO; JPO	2004/06/03 16:07
31	3	(liquid adj crystal adj display or LCD) and scan adj line\$1 and thermistor and resistor and control\$9 and voltage\$1 and change\$9 and temperature and ambient	USPAT; EPO; JPO	2004/06/03 16:07
30	10	(liquid adj crystal adj display or LCD) and scan adj line\$1 and thermistor and resistor	USPAT; EPO; JPO	2004/06/03 16:14
-	809	345/204.ccls.	USPAT; EPO; JPO	2004/01/27 08:16
-	1	5,532,712.pn.	USPAT; EPO; JPO	2003/02/26 11:47
-	277	drive adj circuit and transmissive and liquid adj crystal adj display	USPAT; EPO; JPO	2003/02/26 11:59
-	1	drive adj circuit and transmissive adj scattered and liquid adj crystal adj display	USPAT; EPO; JPO	2003/02/26 11:59
-	15	drive adj circuit and transmissive and liquid adj crystal adj display and ambient adj temperature	USPAT; EPO; JPO	2003/02/26 12:00
-	101079	liquid adj crystal adj display	USPAT; EPO; JPO	2004/06/03 16:02
-	0	liquid adj crystal adj display and charge adj characteristic and compensate	USPAT; EPO; JPO	2003/03/02 14:49
-	4	liquid adj crystal adj display and charge adj characteristic and compensate	USPAT; EPO; JPO	2003/02/26 12:48
-	0	liquid adj crystal adj display and charge adj characteristic and compensate adj circuit	USPAT; EPO; JPO	2003/02/26 12:49
-	4	liquid adj crystal adj display and charge adj characteristic and compensate and circuit	USPAT; EPO; JPO	2003/02/26 12:49
-	3	liquid adj crystal adj display and charge adj characteristic and compensate and circuit and ambient	USPAT; EPO; JPO	2003/02/26 12:49
-	3	liquid adj crystal adj display and charge adj characteristic and compensate and circuit and ambient adj temperature	USPAT; EPO; JPO	2003/02/26 12:50
-	15	liquid adj crystal adj display and charge adj characteristic	USPAT; EPO; JPO	2003/02/26 13:03
-	0	liquid adj crystal adj display and charge adj characteristic and gate adj control	USPAT; EPO; JPO	2003/02/26 13:04
-	0	liquid adj crystal adj display and charge adj characteristic and scanline adj control	USPAT; EPO; JPO	2003/02/26 13:04
-	2	liquid adj crystal adj display and charge adj characteristic and gate and control and temperature	USPAT; EPO; JPO	2003/02/26 13:05
-	2	345/87.ccls. and temperature adj ambient	USPAT; EPO; JPO	2003/02/26 13:08
-	3	liquid adj crystal adj display and charge adj characteristic and gate and control	USPAT; EPO; JPO	2003/02/26 13:11

-	253	345/87.ccls. and temperature	USPAT; EPO; JPO	2003/02/26 13:24
-	14378	345/87.ccls. and temperature and switch and control scan adj line	USPAT; EPO; JPO	2003/02/26 13:25
-	7	345/87.ccls. and temperature and switch and control and scan adj line	USPAT; EPO; JPO	2003/02/26 13:34
-	5	345/204.ccls. and temperature and switch and control and scan adj line	USPAT; EPO; JPO	2003/02/26 13:35
-	117	gate adj line adj driver	USPAT; EPO; JPO	2003/02/26 13:59
-	0	gate adj line adj driver and current adj controller	USPAT; EPO; JPO	2003/02/26 13:59
-	24	gate adj line adj driver and current and controller	USPAT; EPO; JPO	2003/02/26 14:00
-	1	gate adj line adj driver and current and controller and respond and temperature and change	USPAT; EPO; JPO	2003/02/26 14:00
-	3	gate adj line adj driver and current and controller and respond	USPAT; EPO; JPO	2003/02/26 14:46
-	1	gate adj line adj driver and current and controller and respond and temperature	USPAT; EPO; JPO	2003/02/26 14:47
-	20	gate adj line adj driver and change and temperature	USPAT; EPO; JPO	2003/02/26 14:48
-	2	gate adj line adj driver and change adj temperature	USPAT; EPO; JPO	2003/02/26 14:54
-	0	gate adj line adj driver adj respond adj change adj temperature	USPAT; EPO; JPO	2003/02/26 14:56
-	0	gate adj line adj driver and respond adj change adj temperature	USPAT; EPO; JPO	2003/02/26 14:56
-	56	respond adj change adj temperature	USPAT; EPO; JPO	2003/02/26 14:56
-	2	respond adj change adj temperature and current adj controller	USPAT; EPO; JPO	2003/02/26 15:00
-	4115	current adj controller	USPAT; EPO; JPO	2004/01/26 09:40
-	2	current adj controller and respond adj change adj temperature	USPAT; EPO; JPO	2003/02/26 15:02
-	0	current adj controller adj respond adj (change adj temperature)	USPAT; EPO; JPO	2003/02/26 15:03
-	0	current adj controller adj respond and (change adj temperature)	USPAT; EPO; JPO	2003/02/26 15:03
-	81	current adj controller and (change adj temperature)	USPAT; EPO; JPO	2003/02/26 15:04
-	1	5,754,154.pn.	USPAT; EPO; JPO	2003/02/27 13:26
-	1	5,852,430.pn.	USPAT; EPO; JPO	2003/02/27 13:27
-	1	6,075,511.pn.	USPAT; EPO; JPO	2003/02/27 13:28
-	1	6,313,818 B1.pn.	USPAT; EPO; JPO	2003/02/27 13:28
-	2	345/94-101.ccls. and temperature adj detection and current adj detection	USPAT; EPO; JPO	2003/02/27 13:31
-	927	345/97.ccls.	USPAT; EPO; JPO	2003/03/01 15:44
-	157	345/97.ccls. and current controller and resistor and thermistor and TFT	USPAT; EPO; JPO	2003/03/01 15:44
-	156	345/97.ccls. and current controller and resistor and thermistor and TFT and transistor	USPAT; EPO; JPO	2003/03/01 15:44
-	0	345/97.ccls. and current adj controller and resistor and thermistor and TFT and transistor	USPAT; EPO; JPO	2003/03/01 15:46
-	0	345/97.ccls. and current adj controller and resistor and thermistor and transistor	USPAT; EPO; JPO	2003/03/01 15:45
-	0	345/97.ccls. and current adj controller and resistor and thermistor	USPAT; EPO; JPO	2003/03/01 15:45
-	0	345/97.ccls. and current adj controller and resistor	USPAT; EPO; JPO	2003/03/01 15:45

-	0	345/97.ccls. and current adj controller	USPAT; EPO; JPO	2003/03/01 15:45
-	0	345/97.ccls. and current and controller and resistor and thermistor and TFT and transistor	USPAT; EPO; JPO	2003/03/01 15:46
-	0	345/97.ccls. and controller and resistor and thermistor and TFT and transistor	USPAT; EPO; JPO	2003/03/01 15:46
-	0	345/97.ccls. and controller and resistor and thermistor and transistor	USPAT; EPO; JPO	2003/03/01 15:46
-	2	345/97.ccls. and controller and resistor and transistor	USPAT; EPO; JPO	2003/03/01 15:46
-	0	345/97.ccls. and controller and resistor and transistor and thermistor	USPAT; EPO; JPO	2003/03/01 15:46
-	0	current adj controller and resistor and thermistor and TFT and transistor	USPAT; EPO; JPO	2003/03/01 15:47
-	50	current adj controller and resistor and thermistor and transistor	USPAT; EPO; JPO	2003/03/01 15:48
-	0	current adj controller and resistor and thermistor and transistor and LCD	USPAT; EPO; JPO	2003/03/01 15:47
-	0	current adj controller and resistor and thermistor and transistor and liquid adj crystal adj display	USPAT; EPO; JPO	2003/03/01 15:47
-	1326	controller and resistor and transistor and thermistor	USPAT; EPO; JPO	2003/03/01 15:48
-	50	current adj controller and resistor and thermistor and transistor and temperature and voltage	USPAT; EPO; JPO	2003/03/01 15:55
-	20	current adj controller and resistor and thermistor and transistor and temperature and voltage and display	USPAT; EPO; JPO	2003/07/14 09:15
-	0	current adj controller and resistor and thermistor and transistor and temperature and voltage and display and liquid adj crystal	USPAT; EPO; JPO	2003/03/01 15:49
-	20	current adj controller and resistor and thermistor and transistor and temperature and voltage and display	USPAT; EPO; JPO	2003/03/01 15:49
-	0	current adj controller and resistor and thermistor and matrix and TFT and temperature and voltage	USPAT; EPO; JPO	2003/03/01 15:56
-	3	current adj controller and resistor and thermistor and matrix and temperature and voltage	USPAT; EPO; JPO	2003/03/01 15:57
-	0	current adj controller and resistor and thermistor and matrix and gate and source and drain and voltage	USPAT; EPO; JPO	2003/03/01 15:57
-	0	current adj controller and resistor and thermistor and matrix and gate and source and drain	USPAT; EPO; JPO	2003/03/01 15:57
-	93	resistor and thermistor and matrix and gate and source and drain	USPAT; EPO; JPO	2003/03/01 15:58
-	415	resistor and thermistor and LCD	USPAT; EPO; JPO	2003/03/01 15:58
-	0	resistor and thermistor and LCD and gate adj control	USPAT; EPO; JPO	2003/03/01 15:58
-	220	resistor and thermistor and LCD and gate and control	USPAT; EPO; JPO	2003/03/03 08:24
-	111	resistor and thermistor and LCD and gate and control and drain	USPAT; EPO; JPO	2003/03/01 15:59
-	6	resistor and thermistor and LCD and gate and control and drain and source and pixels	USPAT; EPO; JPO	2003/03/03 10:29
-	2	resistor and thermistor and LCD and gate and control and matrix and TFT	USPAT; EPO; JPO	2003/03/01 16:09
-	2	resistor and thermistor and LCD and matrix and TFT	USPAT; EPO; JPO	2003/07/14 17:48
-	107	resistor and thermistor and LCD and gate and control and drain and source	USPAT; EPO; JPO	2003/03/02 14:35
-	4	liquid adj crystal adj display and charge adj characteristic and compensate	USPAT; EPO; JPO	2003/03/02 14:50

-	0	liquid adj crystal adj display and charge adj characteristic and compensate and thermistor	USPAT; EPO; JPO	2003/03/02 14:50
-	0	liquid adj crystal adj display and charge adj characteristic and compensate and temperature adj control	USPAT; EPO; JPO	2003/03/02 14:51
-	5	liquid adj crystal adj display and charge adj characteristic and gate	USPAT; EPO; JPO	2003/03/02 15:07
-	0	liquid adj crystal adj display and charge adj characteristic adj compensator	USPAT; EPO; JPO	2003/03/02 15:49
-	0	liquid adj crystal adj display and charge adj characteristic adj compensate	USPAT; EPO; JPO	2003/03/02 15:49
-	4	liquid adj crystal adj display and charge adj characteristic and compensate	USPAT; EPO; JPO	2003/03/02 15:50
-	10	resistor and thermistor and positive adj temperature adj coefficient and LCD and gate and control	USPAT; EPO; JPO	2003/03/03 07:52
-	0	thermistor and LCD and gate and control and positive adj temperature adj coefficient	USPAT; EPO; JPO	2003/03/03 08:26
-	0	thermistor and LCD and positive adj temperature adj coefficient	USPAT; EPO; JPO	2003/03/03 08:25
-	1860	thermistor and positive adj temperature adj coefficient	USPAT; EPO; JPO	2003/03/03 08:26
-	10	thermistor and LCD and gate and control and positive adj temperature adj coefficient	USPAT; EPO; JPO	2003/03/03 08:26
-	0	thermistor and LCD and gate and control and positive adj temperature adj coefficient and 345/\$	USPAT; EPO; JPO	2003/03/03 08:26
-	10	thermistor and LCD and gate and control and positive adj temperature adj coefficient	USPAT; EPO; JPO	2003/03/03 08:33
-	13916509	thermistor and LCD and gate and control and positive adj temperature adj coefficient and resistor	USPAT; EPO; JPO	2003/03/03 08:33
-	10	thermistor and LCD and gate and control and positive adj temperature adj coefficient and resistor	USPAT; EPO; JPO	2003/03/03 08:33
-	10	thermistor and LCD and gate and control and positive adj temperature adj coefficient and resistor and parallel	USPAT; EPO; JPO	2003/03/03 08:34
-	10	thermistor and LCD and gate and control and positive adj temperature adj coefficient and resistor and parallel and switch	USPAT; EPO; JPO	2003/03/03 08:34
-	12	liquid adj crystal adj display and positive adj temperature adj coefficient adj thermistor	USPAT; EPO; JPO	2003/03/03 10:09
-	0	liquid adj crystal adj display and positive adj temperature adj coefficient adj thermistor and active adj matrix	USPAT; EPO; JPO	2003/03/03 10:10
-	0	liquid adj crystal adj display and positive adj temperature adj coefficient adj thermistor and TFT	USPAT; EPO; JPO	2003/03/03 10:10
-	0	liquid adj crystal adj display and positive adj temperature adj coefficient adj thermistor and gate adj control	USPAT; EPO; JPO	2003/03/03 10:11
-	0	resistor and thermistor and LCD and gate and control and drain and source and pixels and positive adj temperature adj coefficient adj thermistor	USPAT; EPO; JPO	2003/03/03 10:30
-	12	liquid adj crystal adj display and positive adj temperature adj coefficient adj thermistor	USPAT; EPO; JPO	2003/03/03 14:24
-	237	320/116.ccls.	USPAT; EPO; JPO	2003/03/03 14:24
-	0	320/116.ccls. and positive adj temperature adj coefficient adj thermistor	USPAT; EPO; JPO	2003/03/03 14:26

-	1	320,116.ccls. and positive adj temperature adj coefficient adj thermistor	USPAT; EPO; JPO	2003/03/03 14:27
-	1	320/116,128.ccls. and positive adj temperature adj coefficient adj thermistor	USPAT; EPO; JPO	2003/03/03 14:28
-	33	batteries and positive adj temperature adj coefficient adj thermistor	USPAT; EPO; JPO	2003/03/03 14:28
-	3	batteries and positive adj temperature adj coefficient adj thermistor and LCD	USPAT; EPO; JPO	2003/03/03 14:28
-	0	011273973A	USPAT; EPO; JPO	2003/03/04 09:06
-	0	11273973A	USPAT; EPO; JPO	2003/03/04 09:06
-	13912690	"11273973" A	USPAT; EPO; JPO	2003/03/04 09:12
-	1	"11273973"	JPO	2003/03/04 09:13
-	0	JP011273973A	JPO	2003/03/04 09:18
-	1	jp-01127973-\$.did.	JPO	2003/03/04 09:18
-	10580	gate adj voltage and current and change	USPAT; EPO; JPO	2003/07/11 09:59
-	1873	gate adj voltage and current and change and controller	USPAT; EPO; JPO	2003/07/11 10:00
-	0	gate adj voltage and current and change and controller and resistor and thermistor and compensat\$4 and charge adj characteristic	USPAT; EPO; JPO	2003/07/11 10:01
-	6	gate adj voltage and current and change and controller and resistor and thermistor and compensat\$4 and charge and characteristic and circuit and ambient adj temperature	USPAT; EPO; JPO	2003/07/11 10:17
-	11	gate adj voltage and current and change and controller and resistor and thermistor and compensat\$4 and charge and characteristic and circuit	USPAT; EPO; JPO	2003/07/11 10:04
-	3	gate adj voltage and current and change and controller and resistor and thermistor and compensat\$4 and charge and characteristic and circuit and ambient adj temperature and display	USPAT; EPO; JPO	2003/07/11 11:05
-	796275	ambient adj temperature and gate adj voltage and current and change and controller and resistor and thermistor and compensat\$4 and charge and characteristic and circuit and display	USPAT; EPO; JPO	2003/07/11 11:06
-	3	ambient adj temperature and gate adj voltage and current and change and controller and resistor and thermistor and compensat\$4 and charge and characteristic and circuit and display	USPAT; EPO; JPO	2003/07/11 11:06
-	11	gate adj voltage and current and change and controller and resistor and thermistor and compensat\$4 and charge and characteristic	USPAT; EPO; JPO	2003/07/11 11:07
-	40	gate adj voltage and current and change and controller and resistor and thermistor and compensat\$4	USPAT; EPO; JPO	2003/07/11 11:07
-	72	gate adj voltage and current and change and controller and resistor and thermistor	USPAT; EPO; JPO	2003/07/11 11:08
-	0	liquid adj crystal adj display and charge adj characteristic adj compensat\$4 adj circuit	USPAT; EPO; JPO	2003/07/11 14:44
-	0	liquid adj crystal adj display and charge adj characteristic and compensat\$4 adj circuit	USPAT; EPO; JPO	2003/07/11 14:43

-	0	charge adj characteristic adj compensat\$4 adj circuit	USPAT; EPO; JPO	2003/07/11 14:44
-	0	charge adj characteristic adj compensating adj circuit	USPAT; EPO; JPO	2003/07/11 14:44
-	1	liquid adj crystal adj display and charge and characteristic adj compensat\$4 adj circuit	USPAT; EPO; JPO	2003/07/11 14:45
-	59	liquid adj crystal adj display and charge and characteristic and compensat\$4 adj circuit	USPAT; EPO; JPO	2003/07/11 14:45
-	11	liquid adj crystal adj display and charge and characteristic and compensat\$4 adj circuit and thermistor	USPAT; EPO; JPO	2003/07/11 14:46
-	11	liquid adj crystal adj display and charge and characteristic and compensat\$4 adj circuit and thermistor and resistor	USPAT; EPO; JPO	2003/11/20 08:24
-	7	liquid adj crystal adj display and charge and characteristic and compensat\$4 adj circuit and thermistor and resistor and parallel	USPAT; EPO; JPO	2003/07/11 14:47
-	7	liquid adj crystal adj display and charge and characteristic and compensat\$4 adj circuit and thermistor\$1 and resistor and parallel	USPAT; EPO; JPO	2003/07/11 16:27
-	0	charge adj characteristic adj compensating adj circuit	USPAT; EPO; JPO	2003/07/11 16:28
-	1	charge adj characteristic and compensating adj circuit	USPAT; EPO; JPO	2003/07/11 16:30
-	11	charge adj compensating adj circuit	USPAT; EPO; JPO	2003/07/11 16:30
-	0	charge adj compensating adj circuit and Liquid adj crystal adj display	USPAT; EPO; JPO	2003/07/11 16:31
-	1	charge adj compensating adj circuit and display	USPAT; EPO; JPO	2003/07/11 16:31
-	0	charge adj compensating adj circuit and display and temperature	USPAT; EPO; JPO	2003/07/11 16:32
-	4	charge adj compensating adj circuit and temperature	USPAT; EPO; JPO	2003/07/14 08:19
-	0	charge adj compensating adj circuit and temperature and thermistor and resistor	USPAT; EPO; JPO	2003/07/14 08:20
-	0	charge adj compensating adj circuit and temperature and thermistor	USPAT; EPO; JPO	2003/07/14 08:20
-	1	charge adj compensating adj circuit and temperature and resistor	USPAT; EPO; JPO	2003/07/14 08:20
-	1	5,250,937.pn.	USPAT; EPO; JPO	2003/07/14 09:11
-	0	5,250,937.pn. and current adj controller	USPAT; EPO; JPO	2003/07/14 09:11
-	0	5,250,937.pn. and current and controller	USPAT; EPO; JPO	2003/07/14 09:12
-	1	5,250,937.pn. and current	USPAT; EPO; JPO	2003/07/14 09:12
-	1	5,250,937.pn. and current and control	USPAT; EPO; JPO	2003/07/14 09:12
-	1	5,250,937.pn. and current and control\$4	USPAT; EPO; JPO	2003/07/14 13:55
-	1	current adj controller and resistor and thermistor and charge and characteristic and temperature and voltage and display	USPAT; EPO; JPO	2003/07/14 09:15
-	11	current adj controller and resistor and thermistor and charge and temperature and voltage and display	USPAT; EPO; JPO	2003/07/14 09:22
-	0	5,059,767.pn. and current and control\$4	USPAT; EPO; JPO	2003/07/14 13:56
-	1	5,059,767.pn. and current	USPAT; EPO; JPO	2003/07/14 17:35
-	1	5,754,154.pn. and current	USPAT; EPO; JPO	2003/07/14 17:36

-	50	current adj controller and resistor and thermistor and transistor and temperature	USPAT; EPO; JPO	2003/07/14 18:24
-	2	liquid adj crystal adj display and charge and characteristic and compensat\$4 adj circuit and thermistor and resistor and change adj temperature	USPAT; EPO; JPO	2003/11/20 08:25
-	1	liquid adj crystal adj display and charge and characteristic and compensat\$4 adj circuit and thermistor and resistor and change adj temperature and width	USPAT; EPO; JPO	2003/11/20 08:30
-	0	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change adj width	USPAT; EPO; JPO	2003/11/20 08:31
-	33	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change and width	USPAT; EPO; JPO	2003/11/20 08:31
-	0	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change adj current adj path and width	USPAT; EPO; JPO	2003/11/20 08:31
-	0	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change adj path and width and current	USPAT; EPO; JPO	2003/11/20 08:32
-	1	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change and path and width and current and data adj line	USPAT; EPO; JPO	2003/11/20 08:51
-	9	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change and path and width and current and data and line	USPAT; EPO; JPO	2003/11/20 08:37
-	11	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change and path and width and current	USPAT; EPO; JPO	2003/11/20 08:39
-	15	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change and current and gate and driver	USPAT; EPO; JPO	2003/11/20 08:40
-	12	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change and current and gate and driver and data and line	USPAT; EPO; JPO	2003/11/20 08:40
-	12	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change and current and gate and driver and data and line\$1	USPAT; EPO; JPO	2003/11/20 08:42
-	0	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change and current adj controller and gate and driver and data and line\$1	USPAT; EPO; JPO	2003/11/20 08:42
-	0	liquid adj crystal adj display and thermistor and resistor and change and temperature and change and current adj controller and gate and driver and data and line\$1	USPAT; EPO; JPO	2003/11/20 08:42
-	31	liquid adj crystal adj display and thermistor and resistor and change and temperature and change and current adj control\$4 and gate and driver and data and line\$1	USPAT; EPO; JPO	2003/11/20 08:43
-	1	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change and current adj control\$4 and gate and driver and data and line\$1	USPAT; EPO; JPO	2003/11/20 08:43
-	1	5,119,215.pn.	USPAT; EPO; JPO	2003/11/20 09:28

-	398	current adj controller and chang\$5 and width and current and path	USPAT; EPO; JPO	2004/01/26 09:41
-	398	current adj controller and (chang\$5 and width and current and path)	USPAT; EPO; JPO	2004/01/26 09:41
-	19	current adj controller and (chang\$5 and width and current and path) and data adj line\$1	USPAT; EPO; JPO	2004/01/26 11:43
-	0	current adj controller and (chang\$5 and width and current and path) and data adj line\$1 and liquid adj crystal adj cell\$1	USPAT; EPO; JPO	2004/01/26 09:42
-	0	current adj controller and (chang\$5 and width and current and path) and data adj line\$1 and liquid adj crystal adj pixel\$1	USPAT; EPO; JPO	2004/01/26 09:42
-	0	current adj controller and (chang\$5 and width and current and path) and data adj line\$1 and liquid adj crystal adj display	USPAT; EPO; JPO	2004/01/26 09:43
-	3	current adj controller and (chang\$5 and width and current and path) and data adj line\$1 and (liquid adj crystal adj display or LCD)	USPAT; EPO; JPO	2004/01/26 09:45
-	1	current adj controller and (chang\$5 and width adj current adj path)	USPAT; EPO; JPO	2004/01/26 09:47
-	2	(LCD or liquid adj crystal adj display) and (chang\$5 and width adj current adj path)	USPAT; EPO; JPO	2004/01/26 11:14
-	0	(LCD or liquid adj crystal adj display) and (chang\$5 adj width adj current adj path)	USPAT; EPO; JPO	2004/01/26 09:52
-	0	display and (chang\$5 adj width adj current adj path)	USPAT; EPO; JPO	2004/01/26 09:52
-	0	(chang\$5 adj width adj current adj path)	USPAT; EPO; JPO	2004/01/26 09:53
-	0	(chang\$5 adj width adj current adj conductor)	USPAT; EPO; JPO	2004/01/26 09:53
-	0	(var\$6 adj width adj current adj conductor)	USPAT; EPO; JPO	2004/01/26 09:53
-	0	(LCD or liquid adj crystal adj display) and (var\$6 adj width adj current adj path)	USPAT; EPO; JPO	2004/01/26 09:54
-	0	(LCD or liquid adj crystal adj display) and (var\$6 adj width adj current adj path)	USPAT; EPO; JPO	2004/01/26 09:54
-	2	(var\$6 adj width adj current adj path)	USPAT; EPO; JPO	2004/01/26 11:08
-	0	(var\$6 adj width adj current adj conductor)	USPAT; EPO; JPO	2004/01/26 11:09
-	0	(var\$6 adj width\$1 adj current adj conductor\$1)	USPAT; EPO; JPO	2004/01/26 11:09
-	1	(LCD or liquid adj crystal adj display) and (var\$5 and width adj current adj path)	USPAT; EPO; JPO	2004/01/26 11:14
-	0	current adj controller and (chang\$5 adj width and current and path) and data adj line\$1	USPAT; EPO; JPO	2004/01/26 11:44
-	3	current adj controller and (var\$6 adj width and current and path) and data adj line\$1	USPAT; EPO; JPO	2004/01/26 12:19
-	0	current adj controller and (var\$6 adj width adj current adj path) and data adj line\$1	USPAT; EPO; JPO	2004/01/26 11:48
-	0	current adj controller and (var\$6 adj width adj current adj conductor\$1) and data adj line\$1	USPAT; EPO; JPO	2004/01/26 11:48
-	0	current adj controller and (var\$6 adj width adj current adj electrode\$1) and data adj line\$1	USPAT; EPO; JPO	2004/01/26 11:49
-	0	current adj controller and (var\$6 adj width adj current adj electrode\$1)	USPAT; EPO; JPO	2004/01/26 11:49
-	0	current and controller and (var\$6 adj width adj current adj electrode\$1)	USPAT; EPO; JPO	2004/01/26 11:49

-	0	(var\$6 adj width adj current adj electrode\$1)	USPAT; EPO; JPO	2004/01/26 11:49
-	0	current adj controller and (var\$6 adj width and current adj path) and data adj line\$1	USPAT; EPO; JPO	2004/01/26 12:19
-	396	gate adj line\$1 adj driv\$6	USPAT; EPO; JPO	2004/01/26 12:20
-	181	gate adj line\$1 adj driv\$6 and receiv\$6	USPAT; EPO; JPO	2004/01/26 12:21
-	169	gate adj line\$1 adj driv\$6 and receiv\$6 and control\$6	USPAT; EPO; JPO	2004/01/26 12:21
-	118	gate adj line\$1 adj driv\$6 and receiv\$6 and control\$6 and supply\$6	USPAT; EPO; JPO	2004/01/26 12:22
-	81	gate adj line\$1 adj driv\$6 and receiv\$6 and control\$6 and supply\$6 and predetermine\$5	USPAT; EPO; JPO	2004/01/26 12:22
-	26	gate adj line\$1 adj driv\$6 and receiv\$6 and control\$6 and supply\$6 and predetermine\$5 adj voltage\$1	USPAT; EPO; JPO	2004/01/26 12:22
-	8	gate adj line\$1 adj driv\$6 and receiv\$6 and control\$6 and supply\$6 and predetermine\$5 adj voltage\$1 and accord\$6 and control\$6 adj signal\$1	USPAT; EPO; JPO	2004/01/26 12:23
-	8	gate adj line\$1 adj driv\$6 and receiv\$6 and control\$6 and supply\$6 and predetermine\$5 adj voltage\$1 and accord\$6 and control\$6 adj signal\$1 and gate adj Line\$1	USPAT; EPO; JPO	2004/01/26 12:24
-	4	gate adj line\$1 adj driv\$6 and receiv\$6 and control\$6 and supply\$6 and predetermine\$5 adj voltage\$1 and accord\$6 and control\$6 adj signal\$1 and gate adj Line\$1 and (LCD or liquid adj crystal adj panel)	USPAT; EPO; JPO	2004/01/26 12:27
-	1	gate adj line\$1 adj driv\$6 and receiv\$6 and supply\$6 and predetermine\$5 adj voltage\$1 and accord\$6 and control\$6 adj signal\$1 and gate adj Line\$1 adj control\$6 and (LCD or liquid adj crystal adj panel)	USPAT; EPO; JPO	2004/01/26 12:46
-	1	gate adj line\$1 adj driv\$6 and receiv\$6 and supply\$6 and predetermine\$5 adj voltage\$1 and accord\$6 and gate adj Line\$1 adj control\$6 and (LCD or liquid adj crystal adj panel)	USPAT; EPO; JPO	2004/01/26 12:52
-	1	gate adj line\$1 adj driv\$6 adj receiv\$6	USPAT; EPO; JPO	2004/01/26 12:53
-	9	gate adj line\$1 adj (driv\$6 and receiv\$6)	USPAT; EPO; JPO	2004/01/26 12:53
-	2170069	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 signal\$1	USPAT; EPO; JPO	2004/01/26 12:54
-	1	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and (gate adj line\$1 adj control\$6)	USPAT; EPO; JPO	2004/01/26 12:55
-	6	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1	USPAT; EPO; JPO	2004/01/26 12:58
-	6	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and supply\$6	USPAT; EPO; JPO	2004/01/26 12:58
-	0	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and supply\$6 and predetermine\$5 adj voltage\$1	USPAT; EPO; JPO	2004/01/26 12:59
-	6	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and supply\$6 and voltage\$1	USPAT; EPO; JPO	2004/01/26 12:59
-	5	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and supply\$6 and predetermine\$5 and voltage\$1	USPAT; EPO; JPO	2004/01/28 12:41
-	0	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and temperature and supply\$6 and predetermine\$5 and voltage\$1	USPAT; EPO; JPO	2004/01/26 14:00

-	0	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and thermistor and supply\$6 and predetermin\$5 and voltage\$1	USPAT; EPO; JPO	2004/01/26 14:01
-	0	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and thermistor	USPAT; EPO; JPO	2004/01/26 14:01
-	0	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and temperature	USPAT; EPO; JPO	2004/01/26 14:01
-	6	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1	USPAT; EPO; JPO	2004/01/26 16:52
-	0	gate adj line\$1 adj (driv\$6 and receiv\$6) and temperature	USPAT; EPO; JPO	2004/01/26 16:53
-	10900	current adj path and temperature	USPAT; EPO; JPO	2004/01/26 16:53
-	645	current adj path and temperature and thermistor	USPAT; EPO; JPO	2004/01/26 16:53
-	530	current adj path and temperature and thermistor and resistor	USPAT; EPO; JPO	2004/01/26 16:54
-	0	current adj path adj wid\$5 and temperature and thermistor and resistor	USPAT; EPO; JPO	2004/01/26 16:54
-	0	current adj path adj wide\$5 and temperature and thermistor and resistor	USPAT; EPO; JPO	2004/01/26 16:56
-	0	(var\$6 adj width and current adj path) and temperature and thermistor and resistor and gate adj line	USPAT; EPO; JPO	2004/01/26 16:56
-	0	(var\$6 adj width and current adj path) and temperature and thermistor and resistor and gate adj line\$1	USPAT; EPO; JPO	2004/01/26 16:57
-	0	(var\$6 adj width and current adj path) and temperature and thermistor and resistor and (LCD or liquid adj crystal adj display)	USPAT; EPO; JPO	2004/01/26 16:57
-	8	(var\$6 adj width and current adj path) and temperature and thermistor and resistor	USPAT; EPO; JPO	2004/01/26 16:59
-	0	(var\$6 adj width adj current adj path) and temperature and thermistor and resistor	USPAT; EPO; JPO	2004/01/26 16:59
-	0	(var\$6 adj width adj path and current) and temperature and thermistor and resistor	USPAT; EPO; JPO	2004/01/26 16:59
-	939	345/204.ccls.	USPAT; EPO; JPO	2004/01/27 08:48
-	1741807	temperature	USPAT; EPO; JPO	2004/01/27 08:50
-	44	temperature and width adj current adj path	USPAT; EPO; JPO	2004/01/27 15:02
-	0	temperature and width adj current adj path adj change\$4	USPAT; EPO; JPO	2004/01/27 08:51
-	2	temperature and width adj current adj path adj var\$5	USPAT; EPO; JPO	2004/01/27 08:51
-	6	temperature and width adj current adj path adj increas\$5	USPAT; EPO; JPO	2004/01/27 08:54
-	6	width adj current adj path adj increas\$5	USPAT; EPO; JPO	2004/01/27 08:54
-	111016	width adj current adj path adj increas\$5 and LCD or liquid adj crystal adj display	USPAT; EPO; JPO	2004/01/27 08:55
-	0	width adj current adj path adj increas\$5 and (LCD or liquid adj crystal adj display)	USPAT; EPO; JPO	2004/01/27 08:55
-	0	width adj current adj path adj larg\$5 and (LCD or liquid adj crystal adj display)	USPAT; EPO; JPO	2004/01/27 08:55
-	138150	width adj current adj path adj great\$3and (LCD or liquid adj crystal adj display)	USPAT; EPO; JPO	2004/01/27 08:55
-	0	width adj current adj path adj great\$3 and (LCD or liquid adj crystal adj display)	USPAT; EPO; JPO	2004/01/27 08:55
-	3	temperature and width adj current adj path and display	USPAT; EPO; JPO	2004/01/27 15:02

-	15	gate adj line\$1 adj driv\$6 and receiv\$6 and control\$6 and supply\$6 and predetermine\$5 adj voltage\$1 and accord\$6	USPAT; EPO; JPO	2004/01/28 09:35
-	0	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and supply\$6 and predetermin\$5 and voltage\$1 and temperature	USPAT; EPO; JPO	2004/01/28 12:42
-	5	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and supply\$6 and predetermin\$5 and voltage\$1	USPAT; EPO; JPO	2004/01/28 16:45
-	0	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and supply\$6 and predetermin\$5 adj voltage\$1	USPAT; EPO; JPO	2004/01/28 16:45
-	0	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and predetermin\$5 adj voltage\$1	USPAT; EPO; JPO	2004/01/28 16:45
-	0	gate adj line\$1 adj (driv\$6 and receiv\$6) and predetermin\$5 adj voltage\$1	USPAT; EPO; JPO	2004/01/28 16:46
-	57	gate adj line\$1 adj driv\$6 and predetermin\$5 adj voltage\$1	USPAT; EPO; JPO	2004/01/28 16:46
-	29	gate adj line\$1 adj driv\$6 and receiv\$6 and predetermin\$5 adj voltage\$1	USPAT; EPO; JPO	2004/01/28 16:46
-	9	gate adj line\$1 adj driv\$6 and receiv\$6 and predetermin\$5 adj voltage\$1 and Control\$6 adj signal\$1 and supply\$6	USPAT; EPO; JPO	2004/06/03 14:28